

Sentinel Dual SDU SDU 6000



| INPUT | |
|--------------------|---|
| Dual Input | no |
| Rated power | 6000 VA |
| Voltage | 220 / 230 / 240 V |
| Voltage tolerance | 230 V \pm 20% Full load / 230 V +20% -40% half load |
| Frequency | 50 / 60 Hz \pm 5 |
| Power factor | >0.98 |
| Current distortion | \leq 2% |

| BY PASS | |
|---------------------|---|
| Voltage tolerance | 180 / 264 V (selectable in ECO Mode or SMART ACTIVE Mode) |
| Frequency tolerance | Selected frequency \pm 5% (selectable by user) |

| OUTPUT and INVERTER OUTPUT | |
|---|-----------------------|
| Rated power | 6000 VA |
| Active power | 6000 W |
| Crest factor (I _{peak} /I _{rms}) | 3 : 1 |
| Waveform | Sinusoidal |
| Frequency | 50 / 60 Hz selectable |
| Voltage distortion with non-linear load | <3% |
| Voltage distortion with linear load | <1% |

| BATTERIES | |
|-----------------------|--------------------------------------|
| Type | VRLA AGM maintenance-free lead based |
| Typical recharge time | 4-6 h |

| ENVIRONMENTAL | |
|-----------------------|---|
| Altitude max | 1000 m at nominal power (-1% power for every 100 m above 1000 m) 4000 m maximum |
| Color | Black RAL 9005 |
| Communication | USB / RS232 / slot for communications interface / REPO + Input contact |
| Accessories provided | USB cable; handles kit |
| Protections | Overcurrent - short circuit - overvoltage - undervoltage - temperature - excessive low battery |
| Noise (at 1 m) | <48 [bBA] (ECO Mode) |
| Operating temperature | 0 °C - +40 °C |
| Relative humidity | 5-95% non-condensing |
| Output sockets | Terminal block + 2 IEC 320 C19 - PDIST: Terminal block + 8 IEC 320 C13 + 2 IEC 320 C19 |
| Standards | European directives: L V 2014/35/EU low voltage; EMC 2014/30/EU electromagnetic compatibility -- Standards: Safety IEC EN 62040-1; EMC IEC EN 62040-2; RoHS compliant -- Classification in accordance with IEC 62040-3 VFI - SS - 111 |

| DATA | |
|--------------------|--|
| Weight | 46 kg |
| Dimensions (h w d) | 448x131x640 Tower - 3Ux19"x640 Rack mm |
| Input phases | 1 |
| Output phases | 1 |